

**L-Band Avionics Transistor**

The high power pulsed avionics transistor part number IB1012S1100 is designed for L-Band avionics systems operating at 1025 to 1150 MHz. While operating in class C mode under DME pulse conditions at  $V_{CC}=60V$ , this common base device supplies a minimum of 1100 watts of peak pulse power. It utilizes a low loss internal input impedance matching structure to yield maximum device gain and to ease the implementation of external matching circuitry. The new generation bipolar transistor geometry utilizes a gold metallization system to achieve maximum reliability. Emitter ballast resistance is incorporated on the active cell for optimum thermal distribution and maximum reliability. All devices are 100% screened for large signal RF parameters.



**Silicon Bipolar**

- Ultra-high  $f_T$

**Class C Operation**

- High Efficiency

**Common Base Configuration**

- Single Power Supply

**Gold Metal**

- Maximum Reliability

**Emitter Ballasting**

- Optimum Thermal Distribution

**Internal Impedance Matching**

- Ease of Use
- Ultra-low Loss Design

**Be0 Package**

- Unmatched Thermal Reliability

**RF Test Fixture**

- Broadband
- Matched to 50Ω
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed
- Micro-strip structure on soft pc board with dielectric constant 10.5

*TYPICAL DATA*

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| Device   | Freq (MHz) | $V_{CC}$ (V) | $P_{IN}$ (W) | IRL (dB) | $P_{OUT}$ (W) | $G_P$ (dB) | $I_C$ (A) | $n_c$ (%) | Droop (dB) |
|----------|------------|--------------|--------------|----------|---------------|------------|-----------|-----------|------------|
| D3144-12 | 1025       | 60           | 120          | 10       | 1250          | 10.18      | 43.7      | 47.7      | < 0.1      |
|          | 1090       | 60           | 120          | 10       | 1268          | 10.24      | 41.3      | 51.2      | < 0.1      |
|          | 1150       | 60           | 120          | 25       | 1158          | 9.85       | 37.7      | 51.2      | < 0.1      |

Pulse Format = DME (10μs, 1%)

Power measurements made using Boonton 4500 Peak Power Analyzer

**MAXIMUM RATINGS**

| Screen | Parameter                                    | Symbol    | Min | Max  | Units | Test Conditions |
|--------|--|-----------|-----|------|-------|-----------------|
| BD     | Collector-Emitter Voltage                    | $V_{CES}$ | --  | 85   | V     | --              |
| BD     | Emitter-Base Voltage                         | $V_{EBO}$ | --  | 2    | V     | --              |
| BD     | Storage Temperature Range                    | $T_{STG}$ | -55 | +150 | °C    | --              |
| BD     | Operating Junction Temperature Range         | $T_J$     | -55 | +200 | °C    | --              |
| Note   | Screen 'BD' = parameter qualified By Design. |           |     |      |       |                 |

**THERMAL CHARACTERISTICS**

| Screen | Parameter                                    | Symbol       | Min | Max   | Units | Test Conditions  |
|--------|--|--------------|-----|-------|-------|--|
| BD     | Thermal Resistance                           | $R_{TH(JC)}$ | --  | 0.025 | °C/W  | $V_{CC}=60V$ , Pulse format=10 $\mu$ s, 1%, $T_F=25\pm 5^\circ C$ , $P_{IN}=120W$ , $N_C=45\%$ |
| Note   | Screen 'BD' = parameter qualified By Design. |              |     |       |       |  |

**PROCESSING SPECIFICATIONS**

| Screen | Parameter  | Symbol | Min | Max | Units | Test Conditions                             |
|--------|--|--------|-----|-----|-------|---|
| 100%   | DC Wafer Probe   | --     | --  | --  | --    | Per Integra specification.                  |
| Q1     | Wafer DC and RF Qualification  | --     | --  | --  | --    | Per Integra specification.                  |
| LM     | Wire Bond Strength   | --     | --  | --  | --    | Line monitor per Integra specification.     |
| 100%   | Pre-cap visual inspection  | --     | --  | --  | --    | Per Integra specification                   |
| 100%   | Gross leak test  | --     | --  | --  | --    | MIL-STD-750D, Method 1071, Test Condition C |
| Note   | Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer. |        |     |     |       |   |
| Note   | Screen 'LM' = parameter is qualified by assembly line monitor.                           |        |     |     |       |   |

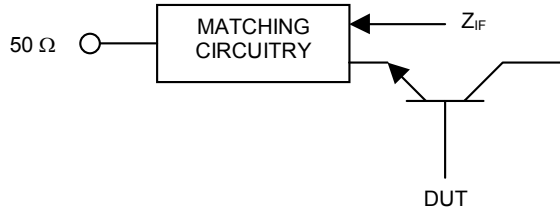
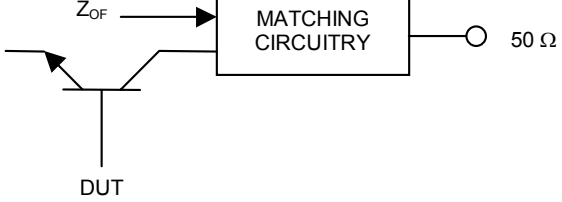
**DC ELECTRICAL CHARACTERISTICS**

| Screen | Parameter                                   | Symbol     | Min | Max | Units | Test Conditions  |
|--------|---|------------|-----|-----|-------|--|
| 100%   | Collector-Emitter Breakdown Voltage         | $BV_{CES}$ | 85  | --  | V     | $I_C = 100mA$ , $V_{BE} = 0V$ , $T_F = 25\pm 5^\circ C$ .  |
| 100%   | Zero Base Voltage Collector Leakage Current | $I_{CES}$  | --  | 20  | mA    | $V_{CE} = 60V$ , $V_{BE} = 0V$ , $T_F = 25\pm 5^\circ C$ . |
| 100%   | DC Current Gain                             | $H_{FE}$   | 10  | 100 | --    | $V_{CE} = 5V$ , $I_C = 500mA$ , $T_F = 25\pm 5^\circ C$ .  |

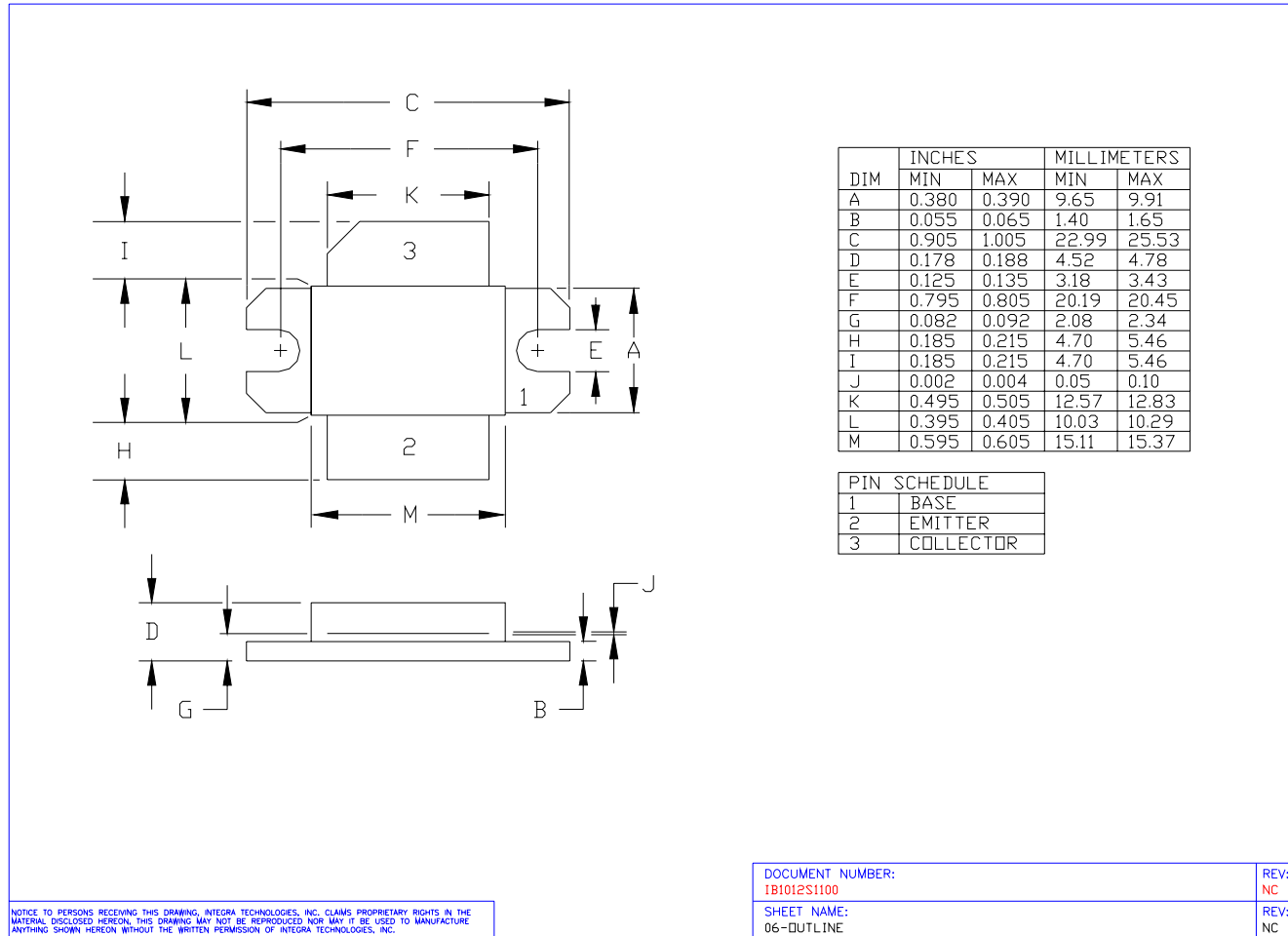
**RF ELECTRICAL CHARACTERISTICS**

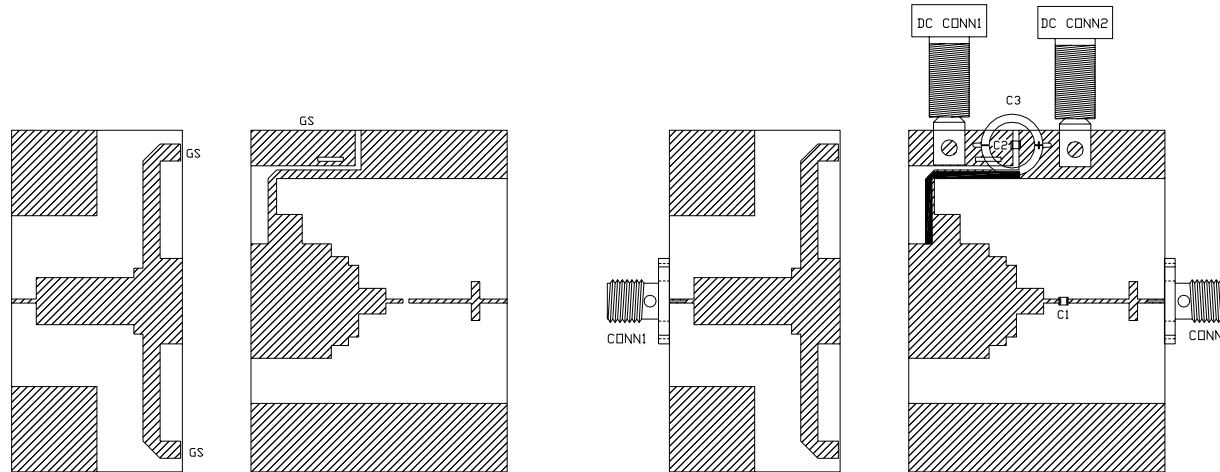
| Screen | Parameter                                    | Symbol        | Min  | Max   | Units | Test Conditions  |
|--------|--|---------------|------|-------|-------|--|
| 100%   | Input Return Loss                            | IRL           | 9    | --    | dB    | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$  |
| 100%   | Maximum Overdrive                            | $P_{IN(MAX)}$ | --   | 160   | W     | $V_{CC}=60V$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .  |
| 100%   | Power Gain                                   | $G_P$         | 9.62 | 11.12 | dB    | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$  |
| 100%   | Collector Efficiency ( $P_O/I_C/V_{CC}$ )    | $N_C$         | 45   | --    | %     | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .  |
| 100%   | Pulse Amplitude Droop                        | D             | --   | 0.5   | dB    | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .  |
| 100%   | Stability into 1.5:1 VSWR                    | VSWR-S        | --   | --    | --    | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .<br>Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. |
| 100%   | Load Mismatch Tolerance                      | LMT           | 3:1  | --    | --    | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .<br>Rotate 3:1 output VSWR through 360° phase. Survival.  |
| BD     | Pulse Risetime                               | RT            | --   | 80    | ns    | $V_{CC}=60V$ , $P_{IN}=120W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .<br>Measure between 10% and 90% detected power points.  |
| Note 1 | F1 = 1025/1090/1150 MHz.                     |               |      |       |       |  |
| Note 2 | Pulse format=DME (10µs, 1%)                  |               |      |       |       |  |
| Note 3 | $T_F$ = Device flange temperature.           |               |      |       |       |  |
| Note 4 | Screen 'BD' = parameter qualified By Design. |               |      |       |       |  |

**RF TEST FIXTURE IMPEDANCE CHARACTERISTICS**

| Frequency (MHz)      | $Z_{IF}$ ( $\Omega$ )  | $Z_{OF}$ ( $\Omega$ )   |
|----------------------|--|---|
| 1025                 | 1.32 - j1.88   | 0.87 - j1.29  |
| 1090                 | 1.18 - j1.18   | 0.81 - j1.02  |
| 1150                 | 1.09 - j0.61   | 0.73 - j0.77  |
| Impedance Definition |  |  |

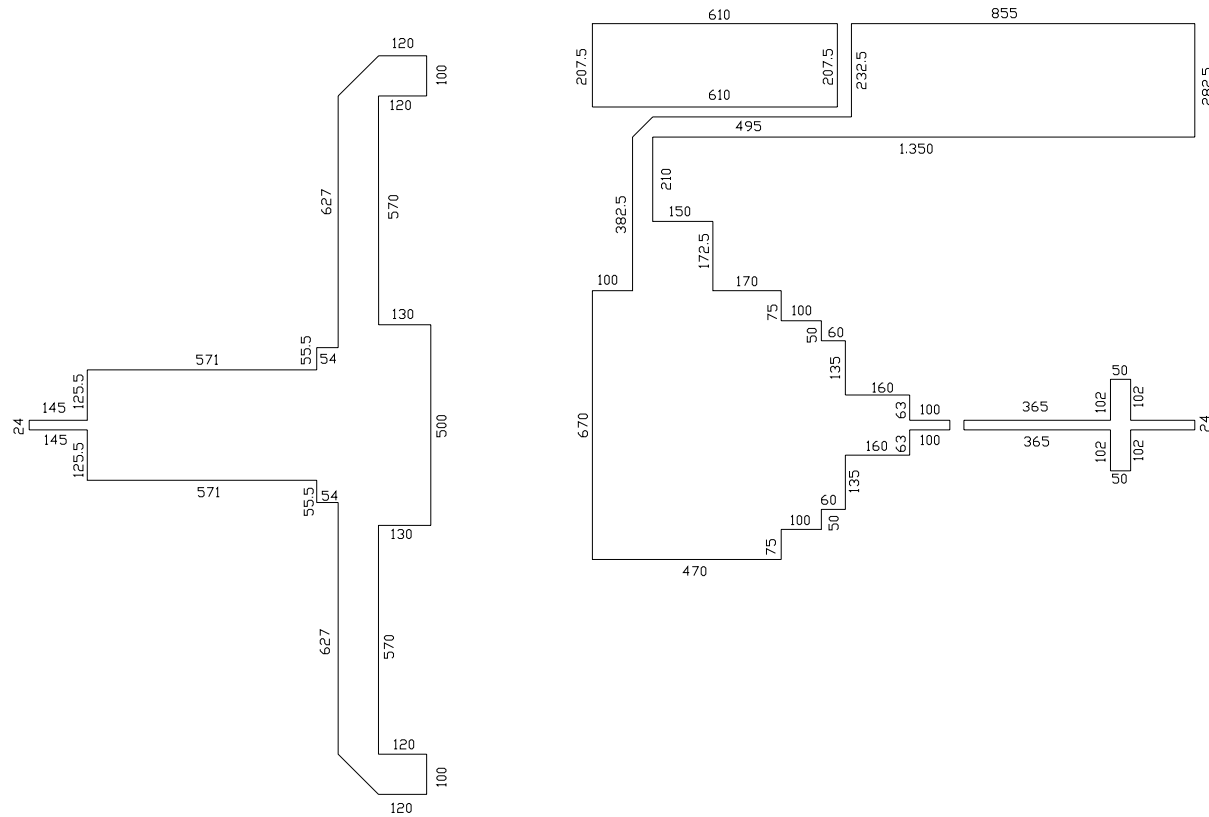
**PACKAGE DIMENSIONAL OUTLINE DRAWING**



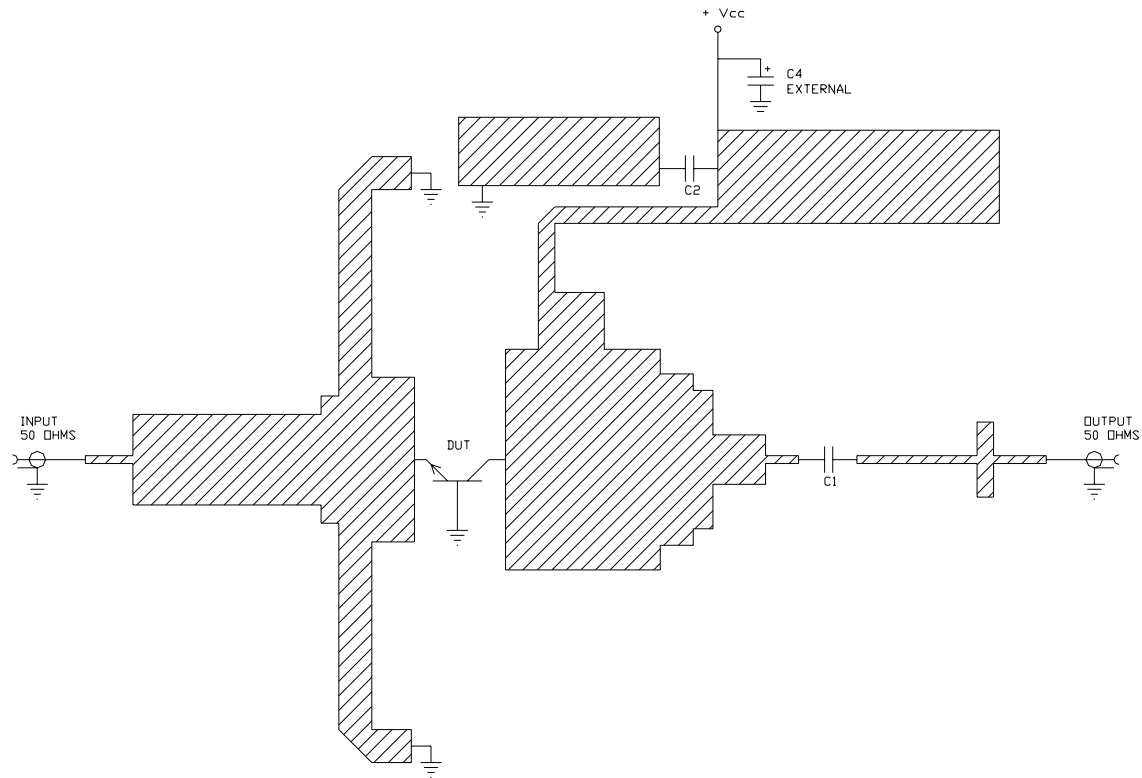


| COMPONENT               | DESCRIPTION  |
|-------------------------|--|
| DUT                     | TRANSISTOR #IB1012S100, MOUNT HARD TO THE RIGHT    |
| PC BOARD                | ROGERS #R03010, TH=0.025" 1E/1E                    |
| C1, C2                  | CHIP CAPACITOR, TYPE ATC100A, 100 pF               |
| C3                      | ELECTROLYTIC CAPACITOR, 68 $\mu$ F / 63V           |
| C4 (NOT SHOWN)          | STORAGE ELECTROLYTIC CAPACITOR, 4700 $\mu$ F / 80V |
| GS                      | GROUND SHIM, COPPER, TH=0.001"                     |
| CONN1, CONN2            | SMA CONNECTOR, TYPE DS #2052-5636-02               |
| INPUT PC BOARD CARRIER  | 2 INCH BRASS - 03 (1.00")                          |
| OUTPUT PC BOARD CARRIER | 2 INCH BRASS - 05 (1.50")                          |
| TRANSISTOR CARRIER      | 2 INCH COPPER - 03                                 |
| TRANSISTOR CLAMP        | NDRYL CLAMP - 04                                   |
| HEATSINK                | 2 INCH HEATSINK - 11                               |
| DC CONN1                | BANANA JACK, BLACK                                 |
| DC CONN2                | BANANA JACK, RED                                   |
| NOTE                    | FIXTURE HARDWARE DRAWINGS AVAILABLE ON REQUEST     |

ASSEMBLY AND PARTS LIST



CIRCUIT DIMENSIONS



ELECTRICAL SCHEMATIC

**DEFINITIONS**

| <b>Data Sheet Status</b>   |   |
|--|---|
| Proposed Specification   | This data sheet contains proposed specifications.                                   |
| Preliminary Specification  | This data sheet contains specifications based on preliminary measurements and data. |
| Product Specification  | This data sheet contains final product specifications.                              |
| <b>Maximum Ratings</b>   |   |
| Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only and operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability. |   |

**WARNING**

| <b>Product and environmental safety - toxic materials</b>  |
|--|
| This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste. |

**DISCLAIMER**

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