

UHF Radar Transistor

The high power pulsed radar transistor device part number IB450S500 is designed for UHF radar systems operating at 450 MHz. While operating in class C mode this common base device supplies a minimum of 500 watts of peak pulse power under the conditions of 30µs pulse width and 10% duty cycle. All devices are 100% screened for large signal RF parameters. Excellent spectral stability into output mismatch over a broad input power range make it ideal for use in reliable high power solid state transmitters.



Silicon Bipolar

- Ultra-high f_T

Class C Operation

- High Efficiency

Common Base Configuration

- Single Power Supply

Gold Metal

- Maximum Reliability

Emitter Ballasting

- Optimum Thermal Distribution

Internal Impedance Matching

- Ease of Use
- Ultra-low Loss Design

BeO Package

- Unmatched Thermal Reliability

RF Test Fixture

- Matched to 50Ω
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed

TYPICAL DATA TYPICAL DATA TYPICAL DATA TYPICAL DATA

Conditions : Pulse: 30us
 Duty: 10%
 Vcc: 40V
 Fixture: ITI 447
 Lot: 2098

Lot/SN	F(MHZ)	Po pk	Pin pk	Ic pk (A)	RL (dB)	Nc (%)	G (dB)	VSWR	
								1.5:1	3:1
2098-1	435	500	66.5	20.67	-17	60	8.8	S	P
	450	500	52.7	18.33	-21	68	9.8	S	P
	465	500	66.5	21.00	-11	60	8.8	S	P

MAXIMUM RATINGS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	V_{CES}	--	80	V	$V_{BE}=0V$.
BD	Emitter-Base Voltage	V_{EBO}	--	3.5	V	--
BD	Storage Temperature Range	T_{STG}	-55	+150	°C	--
BD	Operating Junction Temperature Range	T_J	-55	+200	°C	--
Note	Screen 'BD' = parameter qualified By Design.					

THERMAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	0.20	°C/W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=500W, F=F1$.
Note	Screen 'BD' = parameter qualified By Design.					

PROCESSING SPECIFICATIONS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification.
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071.6, Test Condition C.
Note	Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.					
Note	Screen 'LM' = parameter is qualified by assembly line monitor.					

DC ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Collector-Emitter Breakdown Voltage	BV_{CES}	80	--	V	$I_C=50mA, V_{BE}=0V, T_F=25\pm5^\circ C$.
100%	Zero Base Voltage Collector Leakage Current	I_{CES}	--	10.0	mA	$V_{CE}=40V, V_{BE}=0V, T_F=25\pm5^\circ C$.
100%	DC Current Gain	H_{FE}	10	150	--	$V_{CE}=5V, I_C=0.5A, T_F=25\pm5^\circ C$.

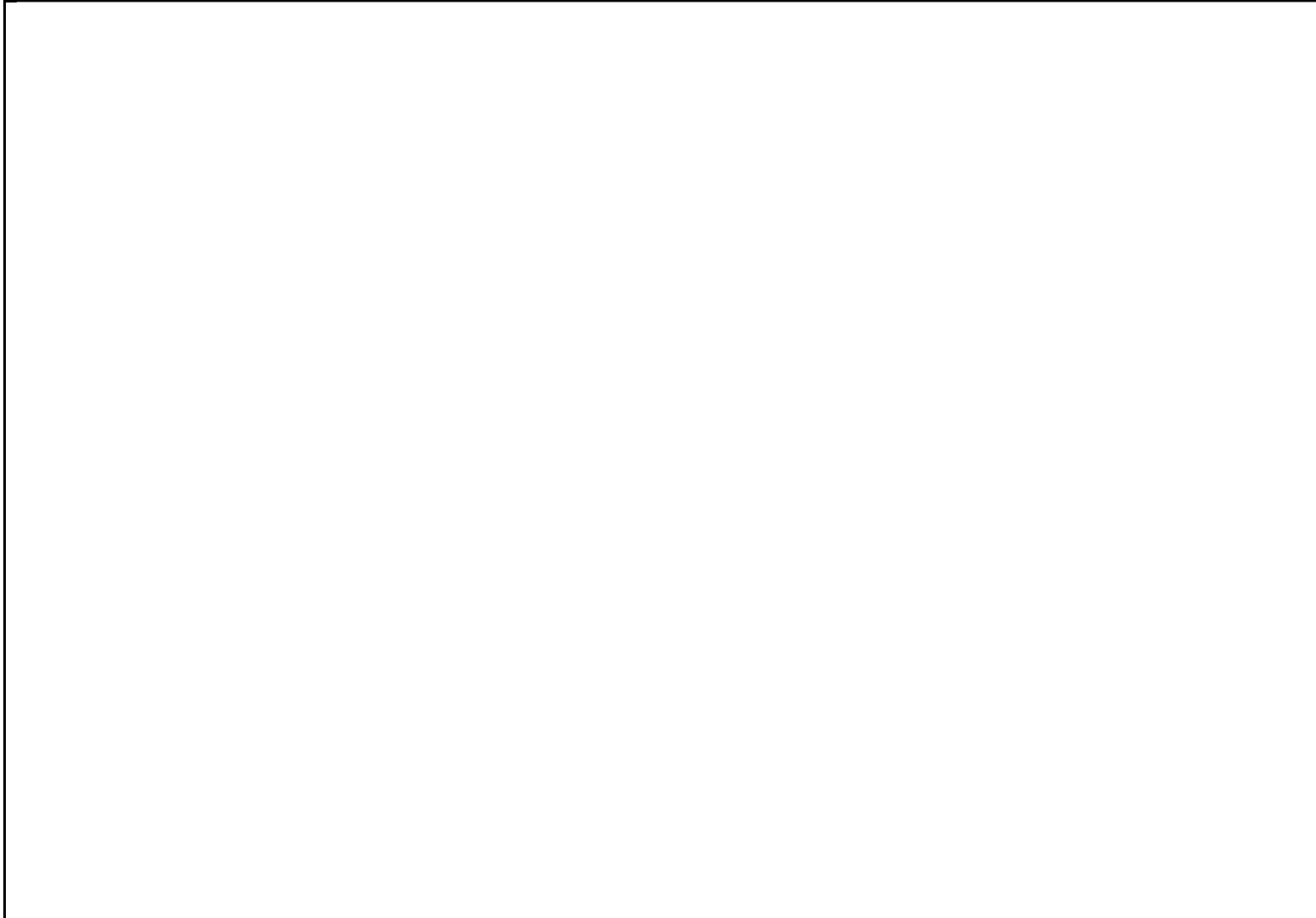
RF ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	10	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$
100%	Input Power	P_{IN}	--	70.6	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$
100%	Power Gain	G_P	8.5	--	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$
100%	Collector Efficiency ($P_o/I_c/V_{CC}$)	N_C	45	--	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$
100%	Pulse Amplitude Droop	D	--	0.5	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$
100%	Stability into 1.5:1 VSWR	VSWR-S	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$ Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. All non-harmonically related signals must be at least -65 dBc.
100%	3:1 Load Mismatch Tolerance	LMT	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$ Rotate 3:1 output VSWR through 360° phase. Post-test $P_{OUT} = \text{Pre-test } P_{OUT} \pm 10W.$
BD	Pulse Risetime	RT	--	150	ns	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}=P_{OUT1}, F=F1.$ Measure between 10% and 90% detected power points.
Note	$V1 = 40V; PW1 = 30\mu s; DF1 = 10\%; P_{OUT1} = 500W; F1 = 450 \text{ MHz}.$					
Note	$T_F = \text{Device flange temperature}.$					
Note	Screen 'BD' = parameter qualified By Design.					

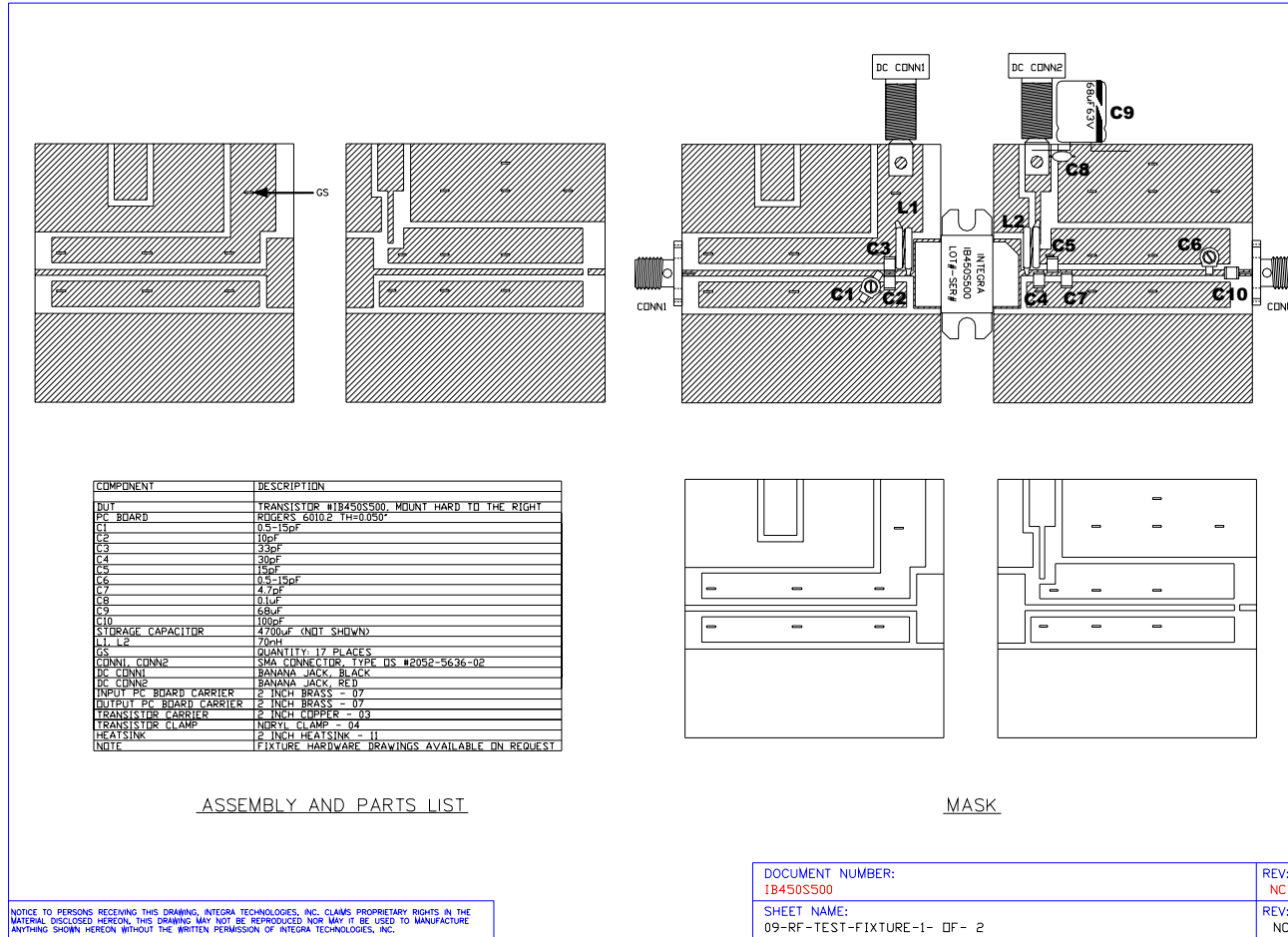
RF TEST FIXTURE IMPEDANCE CHARACTERISTICS

Frequency (MHz)	$Z_{IF} (\Omega)$	$Z_{OF} (\Omega)$
450	$0.96 -j0.42$	$1.55 -j0.47$
Impedance Definition		

PACKAGE DIMENSIONAL OUTLINE DRAWING



RF TEST FIXTURE



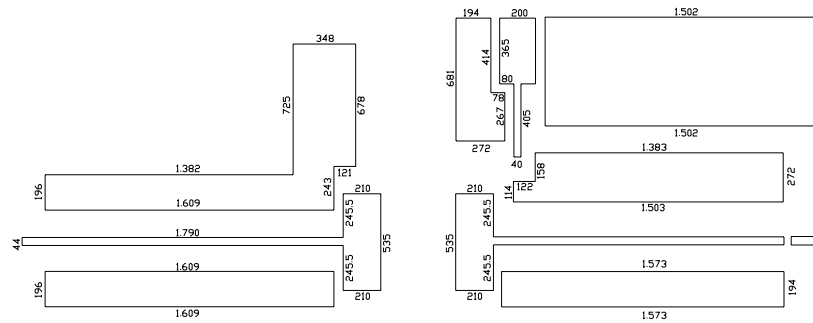
ASSEMBLY AND PARTS LIST

MASK

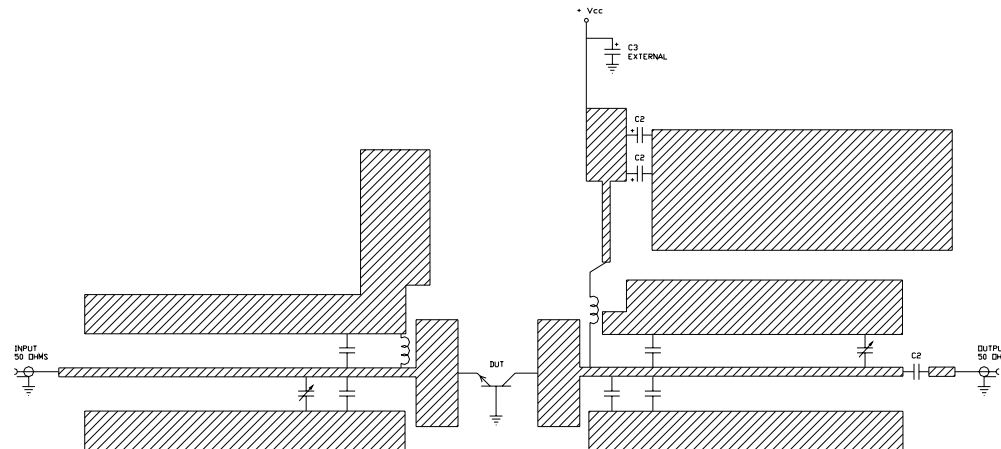
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ASSEMBLY AND PARTS LIST

RF TEST FIXTURE



CIRCUIT DIMENSIONS



ELECTRICAL SCHEMATIC

DEFINITIONS

Data Sheet Status	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
Maximum Ratings	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only and operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

WARNING

Product and environmental safety - toxic materials
This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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