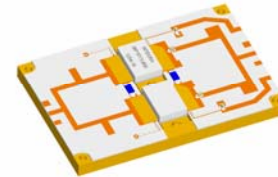


## L-Band Avionics Pallet Amplifier

The high power pulsed avionics pallet part number IBP1011L800 is designed for L-Band avionics systems operating at 1030 MHz. While operating in class C mode under Mode S-ELM pulse burst conditions at  $V_{CC} = 48V$ , this amplifier supplies a minimum of 900 watts of peak pulse power. It utilizes a low loss internal input impedance matching structure to yield maximum device gain and to ease the implementation of external matching circuitry. The new generation bipolar transistor geometry utilizes a gold metallization system to achieve maximum reliability. Emitter ballast resistance is incorporated on the active cell for optimum thermal distribution and maximum reliability. All devices are 100% screened for large signal RF parameters.



### Silicon Bipolar

- Ultra-high  $f_T$

### Class C Operation

- High Efficiency

### Common Base Configuration

- Single Power Supply

### Gold Metal

- Maximum Reliability

### Emitter Ballasting

- Optimum Thermal Distribution

### Impedance Matched to 50Ω

- Ease of Use

### Pallet Carrier

- Ni Plated Copper Carrier
- High Er PCB

### BeO Based Transistor Package

- Unmatched Thermal Reliability

## TYPICAL DATA TYPICAL DATA TYPICAL DATA TYPICAL DATA

Device	Freq (MHz)	$V_{CC}$ (V)	$P_{IN}$ (W)	IRL (dB)	$P_{OUT}$ (W)	$G_p$ (dB)	$I_c$ (A)	$N_c$ (%)
50013488-1	1030	48	100	-17.1	942	9.74	39.22	50.04

Pulse Format = Mode S-ELM - 32μs on/18μs off x 48, repeated at 24ms

Power measurements made using Boonton 4500 Peak Power Analyzer

**MAXIMUM RATINGS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	$V_{CES}$	--	85	V	--
BD	Emitter-Base Voltage	$V_{EBO}$	--	2	V	--
BD	Storage Temperature Range	$T_{STG}$	-55	+150	°C	--
BD	Operating Junction Temperature Range	$T_J$	-55	+200	°C	--
Note	Screen 'BD' = parameter qualified By Design.					

**THERMAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	0.30	°C/W	$V_{CC}=48V$ , Pulse format=Mode S-ELM, $T_F=25\pm 5^\circ C$ , $P_{IN}=100W$ , $N_C=50\%$
Note	Screen 'BD' = parameter qualified By Design.					

**PROCESSING SPECIFICATIONS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071, Test Condition C
Note	Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.					
Note	Screen 'LM' = parameter is qualified by assembly line monitor.					

**RF ELECTRICAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	10	--	dB	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .
100%	Output Power	$P_{OUT}$	900	--	W	$V_{CC}=48V$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .
100%	Power Gain	$G_P$	9.54	11.5	dB	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .
100%	Collector Efficiency ( $P_O/I_C/V_{CC}$ )	$N_C$	45	--	%	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .
100%	Pulse Amplitude Droop	D	--	0.5	dB	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ .
100%	Stability into 1.5:1 VSWR	VSWR-S	--	--	--	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ . Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse.
100%	Load Mismatch Tolerance	LMT	2:1	--	--	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ . Rotate 2:1 output VSWR through 360° phase. Survival.
BD	Pulse Risetime	RT	--	80	ns	$V_{CC}=48V$ , $P_{IN}=100W$ , Pulse = Note 2, $T_F=25\pm 5^\circ C$ , $F=F1$ . Measure between 10% and 90% detected power points.
Note 1	F1 = 1030 MHz.					
Note 2	Pulse format = Mode S-ELM (32µs on / 18µs off x 48, repeat at 24ms)					
Note 3	$T_F$ = Device flange temperature.					
Note 4	Screen 'BD' = parameter qualified By Design.					



**DEFINITIONS**

<b>Data Sheet Status</b>	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
<b>Maximum Ratings</b>	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only and operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

**WARNING**

<b>Product and environmental safety - toxic materials</b>
This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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